	Application No.	Applicant(s)
Notice of Allowability	10/037,680	EMERSON ET AL.
	Examiner	Art Unit	
	Thu Ha T. Nguyen	2155	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *response filed 03/03/06*.

2. ☒ The allowed claim(s) is/are *1,3-10 and 12-20*.

3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a) ☐ All   b) ☐ Some*   c) ☐ None of the:

      1. ☐ Certified copies of the priority documents have been received.

      2. ☐ Certified copies of the priority documents have been received in Application No. _____ .

      3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

    * Certified copies not received: _____ .

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. **THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

    (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached

      1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____ .

    (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____ .

    Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)

2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)

3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____

4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application (PTO-152)

6. ☒ Interview Summary (PTO-413), Paper No./Mail Date *05/11/06* .

7. ☒ Examiner's Amendment/Comment

8. ☒ Examiner's Statement of Reasons for Allowance

9. ☐ Other _____ .

EXAMINER'S AMENDMENT

1.   An examiner's amendment to the record appears below. Should the

changes and/or additions be unacceptable to applicant, an amendment may be filed as

provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be

submitted no later than the payment of the issue fee.

2.   Authorization for this examiner's amendment was given in a telephone

interview with Applicant's representative Mr. Barry D. Blount Reg. No. 35,069, on May

11, 2006.

3.   The application has been amended as follows:

In the claims:

Amend claim 1 as follows:

1.   (Currently amended) A remote server management controller, comprising:

an Input/Output processor (IOP) that is adapted to communicate with a managed

server via a communication interface;

a failsafe register that is adapted to receive an IOP reset request via the

communication interface;

a failsafe boot timer adapted to initiate a countdown when the failsafe register

receives an IOP reset request;

a plurality of registers associated with the IOP, the plurality of registers being

adapted to be accessed by the IOP and to be accessed via the communication

interface, the plurality of registers being adapted to be placed in a locked condition in

which access to the plurality of registers via the communication interface is prevented,

the plurality of registers being adapted to be placed in an unlocked condition in which

access to the plurality of registers via the communication interface is permitted;

wherein the IOP is adapted to be placed in a reset condition if the IOP does not

clear the IOP reset request from the failsafe register and disable the failsafe boot timer

before the countdown expires, and wherein the plurality of registers are placed in the

unlocked condition if the IOP is placed in the reset condition; and

~~a plurality of registers associated with the IOP, the plurality of registers being~~

~~adapted to be accessed by the IOP and to be accessed via the communication~~

~~interface, the plurality of registers being adapted to be placed in a locked condition in~~

~~which access to the plurality of registers via the communication interface is prevented,~~

~~the plurality of registers being adapted to be placed in an unlocked condition in which~~

~~access to the plurality of registers via the communication interface is permitted, and~~

~~wherein the plurality of registers is placed in an unlocked condition if the IOP is placed~~

~~in the reset condition.~~

wherein the IOP is adapted to be placed in an operational condition if the IOP

does clear the IOP reset request from the failsafe register and disable the failsafe boot

timer before the countdown expires, and wherein the plurality of registers are placed in

the locked condition if the IOP is placed in the operational condition.

Amend claim 10 as follows:

10.   (Currently amended) A managed server, comprising:

a communication interface;

a device adapted to communicate via the communication interface;

a remote server management controller, comprising:

an Input/Output processor (IOP) that is adapted to communicate via the communication interface;

a failsafe register that is adapted to receive an IOP reset request via the communication interface;

a failsafe boot timer adapted to initiate a countdown when the failsafe register receives an IOP reset request;

a plurality of registers associated with the IOP, the plurality of registers being adapted to be accessed by the IOP and to be accessed via the communication interface, the plurality of registers being adapted to be placed in a locked condition in which access to the plurality of registers via the communication interface is prevented, the plurality of registers being adapted to be placed in an unlocked condition in which access to the plurality of registers via the communication interface is permitted;

wherein the IOP is adapted to be placed in a reset condition if the IOP does not clear the IOP reset request from the failsafe register and disable the failsafe boot timer before the countdown expires, and wherein the plurality of registers are placed in the unlocked condition if the IOP is placed in the reset condition; and

a plurality of registers associated with the IOP, the plurality of registers being adapted to be accessed by the IOP and to be accessed via the communication interface, the plurality of registers being adapted to be placed in a locked condition in which access to the plurality of registers via the communication interface is prevented,

~~the plurality of registers being adapted to be placed in an unlocked condition in which access to the plurality of registers via the communication interface is permitted, and wherein the plurality of registers is placed in an unlocked condition if the IOP is placed in the reset condition.~~

wherein the IOP is adapted to be placed in an operational condition if the IOP does clear the IOP reset request from the failsafe register and disable the failsafe boot timer before the countdown expires, and wherein the plurality of registers are placed in the locked condition if the IOP is placed in the operational condition.

Amend claim 18 as follows:

18.   (Currently amended) A method of authorizing access to a register, comprising the steps of:

receiving a reset request via a failsafe register;

initiating a countdown of predetermined duration in response to the reset request;

attempting to access a plurality of registers associated with an Input/Output processor (IOP) via a communication interface, the plurality of registers being adapted to be placed in a locked condition in which access to the plurality of registers via the communication interface is prevented, the plurality of registers being adapted to be placed in an unlocked condition in which access to the plurality of registers via the communication interface is permitted;

~~permitting access to the register~~ placing the IOP in a reset condition and the

plurality of registers in the unlocked condition if the reset request is not cleared and the

countdown is ~~not~~ halted before expiration of the predetermined duration; and

~~attempting to access a plurality of registers associated with an Input/Output~~

~~processor (IOP) via a communication interface, the plurality of registers being adapted~~

~~to be placed in a locked condition in which access to the plurality of registers via the~~

~~communication interface is prevented, the plurality of registers being adapted to be~~

~~placed in an unlocked condition in which access to the plurality of registers via the~~

~~communication interface is permitted; and~~

~~wherein the plurality of registers being placed in an unlocked condition if the IOP~~

~~is placed in the reset condition.~~

placing the IOP in an operational condition and the plurality of registers in the

locked condition if the IOP does clear the IOP reset request from the failsafe register

and disable the failsafe boot timer before the countdown expires.

Allowable Subject Matter

4.   Claims 1, 3-10, and 12-20 are allowed.

5.   The following is an examiner's statement of reasons for allowance: The

examiner has found that the prior art of record does not appear to teach or suggest or

render obvious the claimed limitations in combination with the specific added limitations

as recited in independent claims and subsequent dependent claims. The prior art of

record fails to teach or suggest an apparatus and method for authorizing access to a

plurality of registers wherein the plurality of registers associated with an Input/Output

processor (IOP) via a communication interface, the plurality of registers being placed in

a locked condition and unlocked condition, wherein the plurality of registers are placed

in the unlocked condition if the IOP is placed in a reset condition in which the IOP is

adapted to be placed in the reset condition if the IOP does not clear the IOP reset

request from a failsafe register and disable the failsafe boot timer before the countdown

expires; the plurality of registers being placed in an lock condition in which access to

plurality of registers is prevented if the IOP is placed in an operation condition wherein

the IOP is adapted to be placed in an operational condition if the IOP does clear the IOP

reset request from the failsafe register and disable the failsafe boot timer before the

countdown expires. Therefore these features are novel and also in conjunction with all

other limitations in the claims.

Additional references

6.   The examiner as of general interest cites the following references:

A) Benson, IV et al., USPN 6,081,752.

B) Lai et al., USPN 2005/0081084.

C) Abbondanzio et al., USPN 2003/0188222.

D) Benfield et al., USPN 2003/0009551.

E) Ebsen et al., USPN 2003/0217310.

7.   Any comments considered necessary by applicant must be submitted no

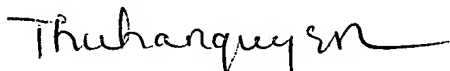later than the payment of the issue fee and, to avoid processing delays, should

preferably accompany the issue fee. Such submissions should be clearly labeled

"Comments on Statement of Reasons for Allowance."

8.   Any inquiry concerning this communication or earlier communications from

the examiner should be directed to Thu Ha Nguyen, whose telephone number is (571)

272-3989. The examiner can normally be reached Monday through Friday from 8:30

AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Najjar Saleh, can be reached at (571) 272-4006.

The fax phone numbers for the organization where this application or proceeding

is assigned are (571) 273-8300 for regular communications.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

ThuHa Nguyen

May 12, 2006